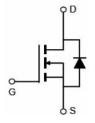


Main Product Characteristics:

V _{DSS}	150V			
R _{DS} (on)	4.8mΩ (typ.)			
I _D	240A			







1O-220 SMS015N07A1

TO-263 SMS015N07D1

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V①	240	
I _D @ T _C = 100°C	T _C = 100°C Continuous Drain Current, V _{GS} @ 10V①		Α
I _{DM}	Pulsed Drain Current②	720	
P _D @T _C = 25°C	Power Dissipation③	272	W
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-to-Source Voltage	± 20	V
E _{AS}	E _{AS} Single Pulse Avalanche Energy @ L=0.5mH		mJ
I _{AS}	I _{AS} Avalanche Current		Α
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C

Version : Preliminary



Thermal Resistance

Symbol	Characteristics	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	0.46	°C/W
R _{θJA}	Junction-to-ambient ④	_	62	C/VV

Electrical Characteristics @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
V _{(BR)DSS}	Drain-to-Source breakdown voltage	150	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$	
R _{DS(on)}	Static Drain-to-Source on-resistance	_	4.8	5.8	mΩ	V _{GS} =10V, I _D =40A	
V _{GS(th)}	Gate threshold voltage	1	_	2.5	V	V _{DS} =V _{GS} , I _D =250uA	
I _{DSS}	Drain-to-Source leakage current Tj=25°C	_	_	1	μA	V _{DS} =140V, V _{GS} =0V	
1	Coto to Source forward lookage	_	_	100	nA	V _{GS} =20V, V _{DS} =0V	
I _{GSS}	Gate-to-Source forward leakage	_	_	-100	IIA	V _{GS} =-20V, V _{DS} =0V	
Qg	Total gate charge	_	66	_		V _{GS} =10V,	
Q _{gs}	Gate-to-Source charge	_	21	_	nC	V _{DS} =75V,	
Q _{gd}	Gate-to-Drain("Miller") charge	_	20	_		I _D =70A	
t _{d(on)}	Turn-on delay time	_	18	_		V _{GS} =10V	
tr	Rise time	_	21	_		V _{DS} =75V	
t _{d(off)}	Turn-Off delay time	_	36	_	ns	R _G =3Ω	
t _f	Fall time	_	10	_		R _L =1.07Ω	
C _{iss}	Input capacitance	_	4196	_		V _{GS} =0V	
Coss	Output capacitance		2875		pF	V _{DS} =25V	
C _{rss}	Reverse transfer capacitance	_	210	_		f=1MHz	

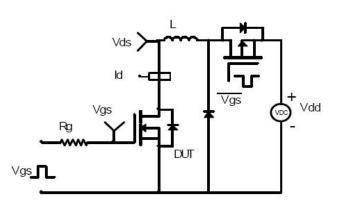
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
1.	Continuous Source Current			240	Α	MOSFET symbol
I _S	(Body Diode)	_	_	240	_ A	showing the
	Pulsed Source Current			720	_	integral reverse
I _{SM}	(Body Diode)	_	_	720	A	p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	1.2	V	I _S =20A, V _{GS} =0V
trr	Reverse Recovery Time	_	101	_	ns	1 - 204 d1/dt-E004/us
Qrr	Reverse Recovery Charge	_	1240	_	nC	l _F = 20A, dl/dt=500A/µs

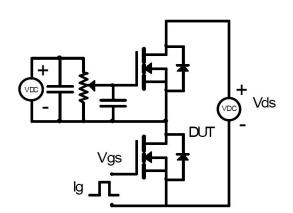
Version : Preliminary

Test Circuits and Waveforms

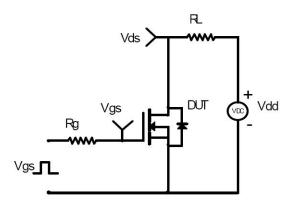
EAS Test Circuit:



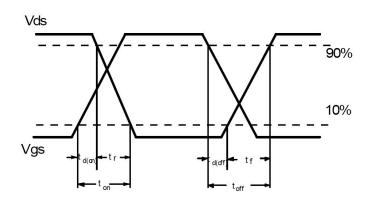
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



Version : Preliminary

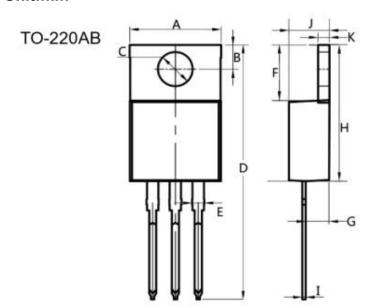
Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.
- 4The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

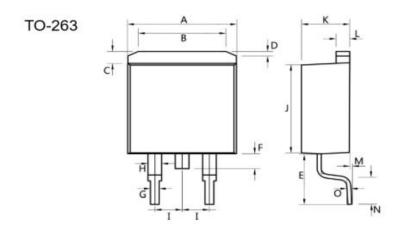
Mechanical Data:

Product ID	Package
SMS015N07A1	TO-220
SMS015N07D1	TO-263

Unit:mm



10.4
3.0
4.0
30.0
1.5
6.6
3.3
16.0
0.45
4.7
1.4
-



Dim.	Min.	Max.		
Α	10.0	10.5		
В	7.25	7.75		
С	1.3	1.5		
D	0.55	0.75		
E	5.0	6.0		
F	1.4	1.6		
G	0.75	0.95		
Н	1.15	1.35		
- 1	Typ 2.54			
J	8.4	8.6		
K	4.4	4.6		
L	1.25	1.45		
М	0.02	0.1		
N	2.4	2.8		
0	0.35	0.45		
All Dim	ensions in m	illimeter		

Version : Preliminary



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