

SSFD3005

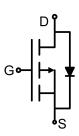
Main Product Characteristics:

V _{DSS}	-30V			
R _{DS} (on)	6.3mΩ(typ)			
Ι _D	-85A			



TO-252





Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units	
I _D @ T _C = 25°C	Continuous Drain Current, V_GS @ 10V (1)	-85		
I _D @ T _C = 70°C	Continuous Drain Current, V_GS @ 10V (1)	-68	Α	
I _{DM}	Pulsed Drain Current	-200		
P _D @T _C = 25°C	Power Dissipation ②	100	W	
V _{DS}	Drain-Source Voltage	-30	V	
V _{GS}	Gate-to-Source VItage	± 25	V	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	

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Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{0JA}	Junction-to-ambient (t $\leq 10s$) (3)		41	°C /W

Electrical Characterizes @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	-30	_		V	V _{GS} = 0V, I _D = -250µA
$R_{\text{DS(on)}}$	Static Drain-to-Source on-resistance		6.3	7.5	mΩ	V _{GS} =-10V,I _D =- 20A
			5.1	6	mΩ	V _{GS} =-20V,I _D =- 20A
g FS	Forward Transconductance	20	_		S	V _{DS} =-5V,I _D =-20A
$V_{GS(th)}$	Gate threshold voltage	-1.5	_	-3.5	V	V _{DS} = V _{GS} , I _D =- 250µA
IDSS	Drain-to-Source leakage current		_	-1	μA	V _{DS} = -24V,V _{GS} = 0V
			_	100		V _{GS} =25V
I _{GSS}	Gate-to-Source forward leakage		_	-100	nA	V _{GS} = -25V
Ciss	Input capacitance		4300			V _{GS} = 0V
Coss	Output capacitance		1000		pF	V _{DS} = -15V
Crss	Reverse transfer capacitance		750			f = 1MHz
Qg	Total gate charge		95			I _D = -20A,
Q _{gs}	Gate-to-Source charge		20	_	nC	V _{DS} =-15V,
Q _{gd}	Gate-to-Drain("Miller") charge		30	_		V _{GS} = -10V
t _{d(on)}	Turn-on delay time	_	20	_		
tr	Rise time	_	30	_		V_{GS} =-10V, V_{DD} =-15V,
t _{d(off)}	Turn-Off delay time	_	50	_	ns	$R_{GEN}=3\Omega, I_D = -1A$
t _f	Fall time	_	35	_		

Source-Drain Ratings and Characteristics

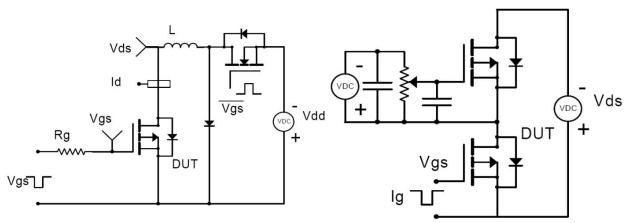
Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current		_	-85	А	MOSFET symbol ঢ
	(Body Diode)					showing the
Ism	Pulsed Source Current	_	_	-200	А	integral reverse G⊶ → ¥
	(Body Diode)					p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	-1	V	I _S =-1A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	_	40		ns	T」= 25°C, I _F =-20A, di/dt =
Q _{rr}	Reverse Recovery Charge	_	30		nC	100A/µs



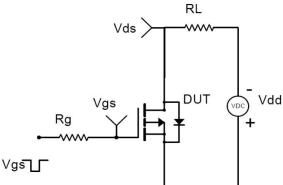
Test Circuits and Waveforms

EAS Test Circuit:

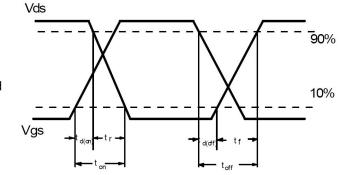
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



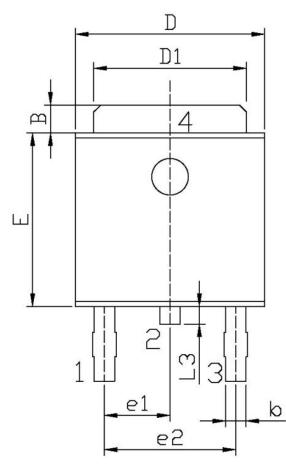
Notes:

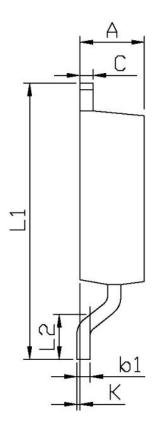
- ①Calculated continuous current based on maximum allowable junction temperature.
- ②Repetitive rating; pulse width limited by max. junction temperature.
- $\textcircled{The power dissipation } P_D$ is based on max. junction temperature, using junction-to-case thermal resistance.



SSFD3005

Mechanical Data: Unit:mm





Symbol	Dimensions In Millimeters		Symbol	Dimensions In Millimeters	
	Min	Max	Symbol	Min	Max
A	2.20	2.40	Е	5.95	6.25
В	0.95	1.25	e1	2.24	2.34
b	0.50	0.90	e2	4.43	4.73
b1	0.45	0.55	L1	9.45	9.95
С	0.45	0.55	L2	1.25	1.75
D	6.45	6.75	L3	0.60	0.90
D1	5.10	5.50	K	0.00	0.10





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