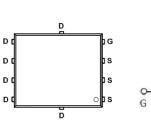


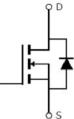
SMT004N03J7

Main Product Characteristics:

V _{DSS}	40V		
R _{DS} (on)	1.7mΩ (typ.)		
Ι _D	180A		







PDFN5x6-8L

Pin Assignments

Schematic Diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Symbol Parameter			
I _D @ T _C = 25°C	Continuous Drain Current, V_GS @ 10V $\textcircled{1}$	180		
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V (1)	107	А	
I _{DM}	Pulsed Drain Current ②	720		
P _D @T _C = 25°C	Power Dissipation ③	100	W	
V _{DS}	Drain-Source Voltage	40	V	
V _{GS}	Gate-to-Source Voltage	± 20	V	
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C	

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Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
R _{θJC}	Junction-to-case ③		1.25	°C/W

Electrical Characteristics @TA=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source breakdown voltage	40	—	—	V	$V_{GS} = 0V, I_D = 250 \mu A$
R _{DS(on)}	Static Drain-to-Source on-resistance	—	1.7	2.4	mΩ	V_{GS} =10V,I _D = 40A
V _{GS(th)}	Gate threshold voltage	2	—	4	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
I _{DSS}	Drain-to-Source leakage current	—	—	1	μA	$V_{DS} = 40V, V_{GS} = 0V$
1		—	—	100		V _{GS} =20V
I _{GSS}	Gate-to-Source forward leakage		—	-100	nA	V _{GS} = -20V
C _{iss}	Input capacitance	—	9792	—		$V_{GS} = 0V$
Coss	Output capacitance	—	660	—	pF	V _{DS} = 30V
C _{rss}	Reverse transfer capacitance	—	524	—		f = 1MHz
Qg	Total gate charge		130	—		I _D = 20A,
Q _{gs}	Gate-to-Source charge		23	—	nC	V _{DS} =20V,
Q _{gd}	Gate-to-Drain("Miller") charge	—	36	—		$V_{GS} = 10V$
t _{d(on)}	Turn-on delay time		35	—		
tr	Rise time		34	_		V_{GS} =10V, V_{DS} =20V,
t _{d(off)}	Turn-Off delay time		67	—	ns	$R_{GEN}=3.6\Omega, R_L=1\Omega$
t _f	Fall time	_	21	_		

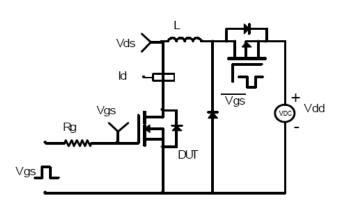
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current (Body Diode)	_	_	180	А	MOSFET symbol showing the
I _{SM}	Pulsed Source Current (Body Diode)	_		720	А	integral reverse p-n junction diode.
V _{SD}	Diode Forward Voltage	_	_	1.2	V	I _S =40A, V _{GS} =0V
t _{rr}	Reverse Recovery Time	—	50	_	ns	$T_J = 25^{\circ}C$, $I_F = 20A$, di/dt =
Qrr	Reverse Recovery Charge	—	75	—	nC	100A/µs

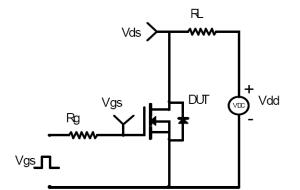


Test Circuits and Waveforms

EAS Test Circuit:

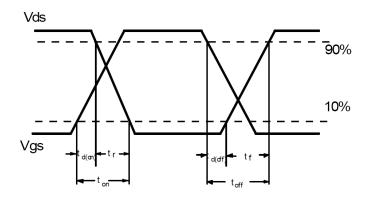


Switching Time Test Circuit:



Switching Waveforms:

Gate Charge Test Circuit:



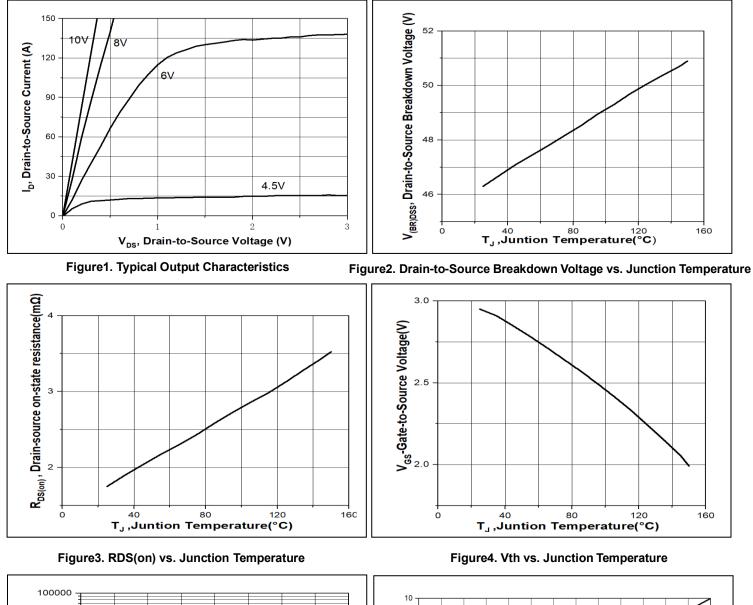
Notes:

- ①Calculated continuous current based on maximum allowable junction temperature.
- 2 Repetitive rating; pulse width limited by max. junction temperature.
- $\ensuremath{\textcircled{3}}$ The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.



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Typical Electrical and Thermal Characteristics



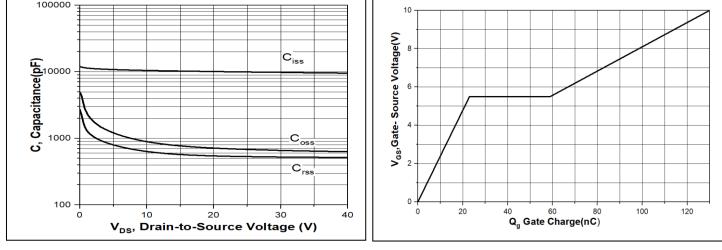


Figure5. Capacitance

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Figure6. Gate Charge



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Typical Electrical and Thermal Characteristics

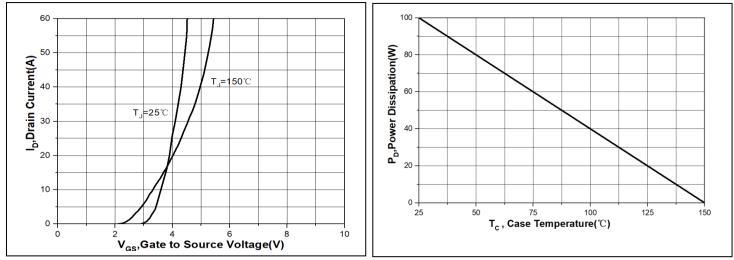
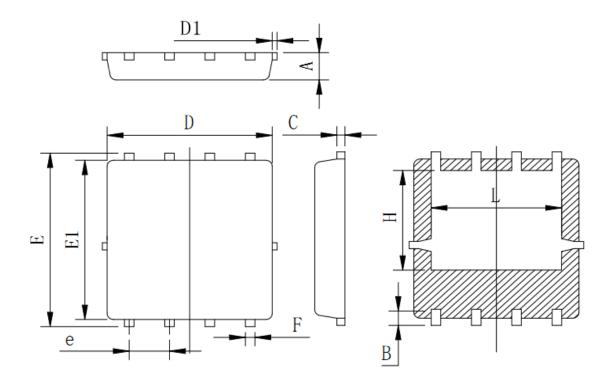


Figure7. Transfer Characteristics

Figure8. Power Dissipation



Mechanical Data:



Symbol	Min	Тур	Max
А	0.90	0.95	1.00
В	0.48	0.58	0.68
С	0.20	0.254	0.30
D	5.00	5.20	5.40
Dl			0.15
E	5.90	6.05	6.20
El	5.40	5.55	5.70
e	1.22	1.27	1.32
F	0.25	0.30	0.35
Н	3.27	3.47	3.67
L	3.80	4.00	4.20



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