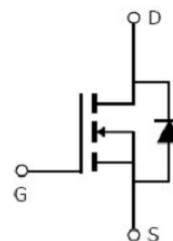


Main Product Characteristics:

V_{DSS}	40V
$R_{DS(on)}$	2.1m Ω (typ.)
I_D	170A


TO-263 (D2PAK)

Schematic Diagram
Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature


Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute Max Rating:

Symbol	Parameter	Max.	Units
I_D @ $T_c = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ①	170	A
I_{DM}	Pulsed Drain Current ②	680	
P_D @ $T_c = 25^\circ\text{C}$	Power Dissipation ③	208	W
V_{DS}	Drain-Source Voltage	40	V
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy @ $L=0.5\text{mH}$	719	mJ
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

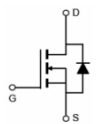
Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-case ③	—	0.72	°C/W

Electrical Characteristics @ $T_A=25^{\circ}\text{C}$ unless otherwise specified

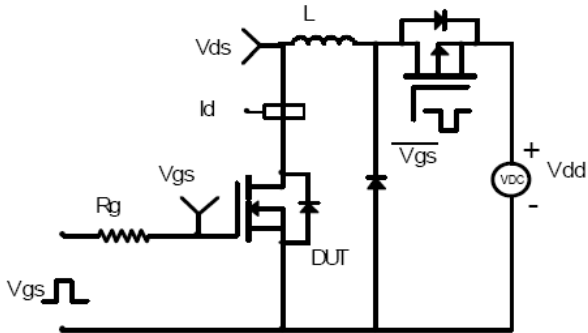
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	40	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$R_{DS(on)}$	Static Drain-to-Source on-resistance	—	2.1	2.4	m Ω	$V_{GS}=10V, I_D = 40A$
$V_{GS(th)}$	Gate threshold voltage	2	—	4	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
I_{DSS}	Drain-to-Source leakage current	—	—	1	μA	$V_{DS} = 40V, V_{GS} = 0V$
I_{GSS}	Gate-to-Source forward leakage	—	—	100	nA	$V_{GS} = 20V$
		—	—	-100		$V_{GS} = -20V$
Q_g	Total gate charge	—	138	—	nC	$I_D = 20A,$ $V_{DS}=20V,$ $V_{GS} = 10V$
Q_{gs}	Gate-to-Source charge	—	25	—		
Q_{gd}	Gate-to-Drain("Miller") charge	—	36	—		
$t_{d(on)}$	Turn-on delay time	—	32	—	ns	$V_{GS}=10V,$ $R_{GEN}=3.6\Omega$ $V_{DS}=20V$ $R_L=1\Omega$
t_r	Rise time	—	33	—		
$t_{d(off)}$	Turn-Off delay time	—	70	—		
t_f	Fall time	—	24	—		
C_{iss}	Input capacitance	—	10973	—	pF	$V_{GS} = 0V$ $V_{DS} = 40V$ $f = 1MHz$
C_{oss}	Output capacitance	—	650	—		
C_{rss}	Reverse transfer capacitance	—	536	—		

Source-Drain Ratings and Characteristics

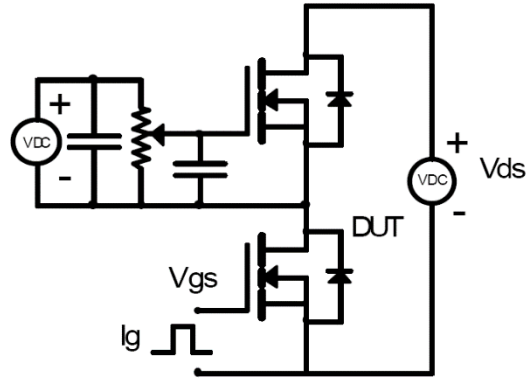
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	170	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode)	—	—	680	A	
V_{SD}	Diode Forward Voltage	—	—	1.2	V	$I_S=40A, V_{GS}=0V$
t_{rr}	Reverse Recovery Time	—	50	—	ns	$I_F=20A, di/dt=100A/us$
Q_{rr}	Reverse Recovery Charge	—	75	—	nC	

Test Circuits and Waveforms

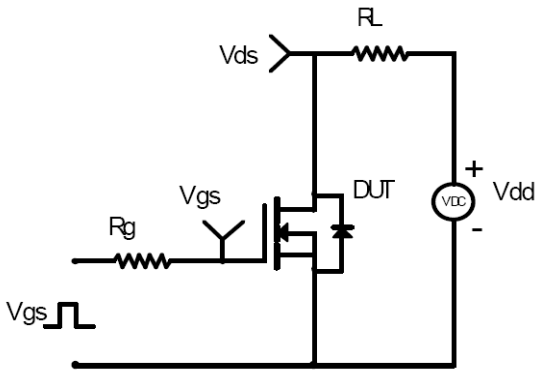
EAS Test Circuit:



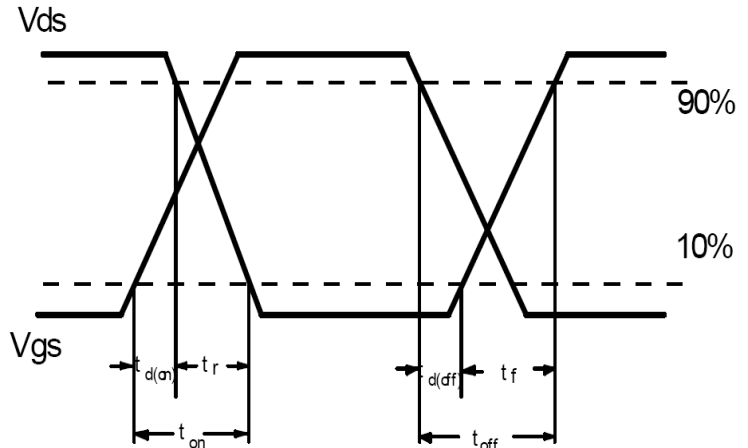
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



Notes:

- ① Calculated continuous current based on maximum allowable junction temperature.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation P_D is based on max. junction temperature, using junction-to-case thermal resistance.

Typical Electrical and Thermal Characteristics

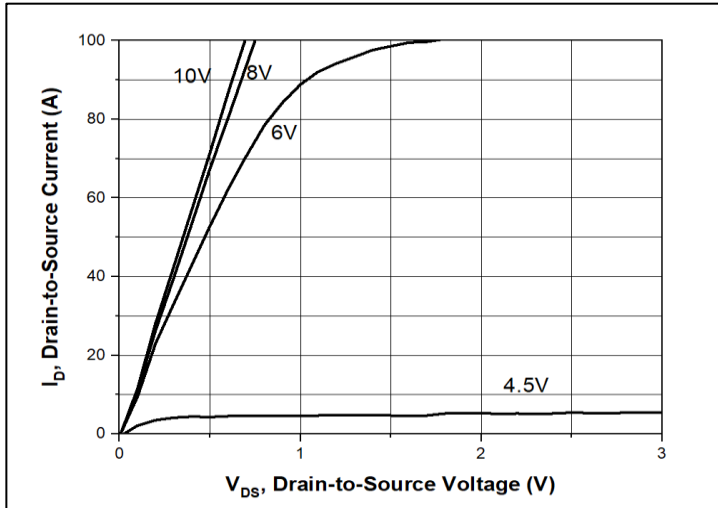


Figure1. Typical Output Characteristics

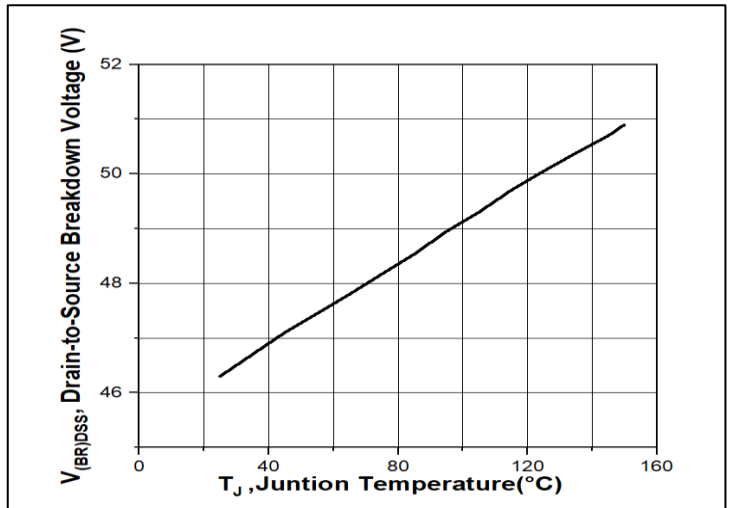


Figure2. Drain-to-Source Breakdown Voltage vs. Junction Temperature

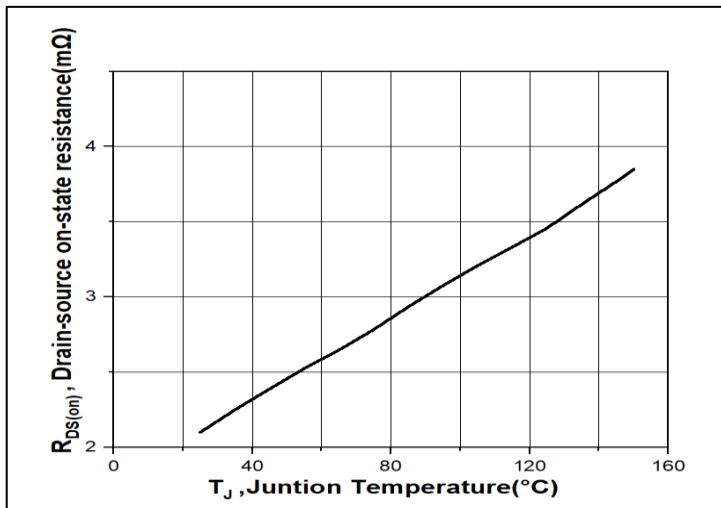


Figure3. RDS(on) vs. Junction Temperature

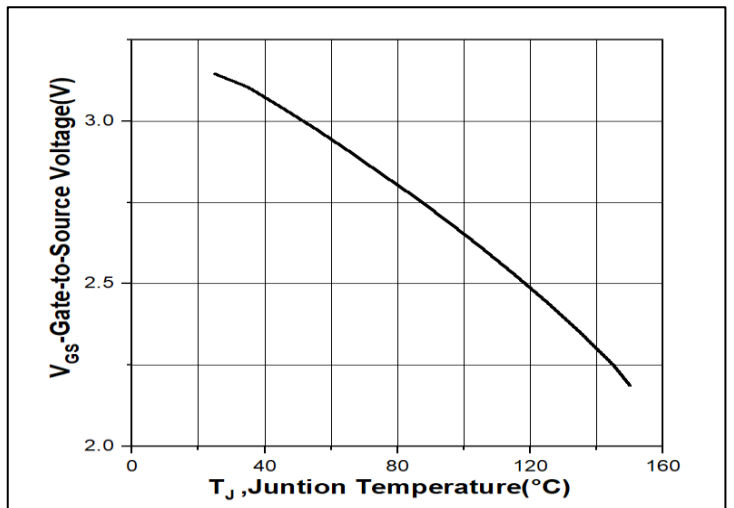


Figure4. Vth vs. Junction Temperature

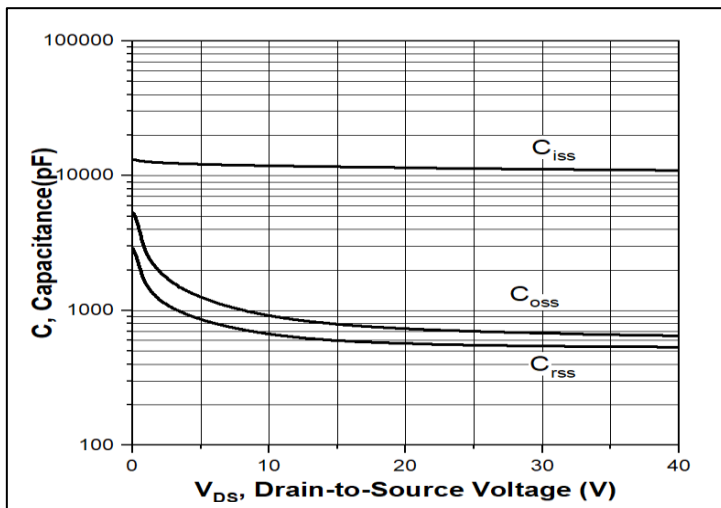


Figure5. Capacitance

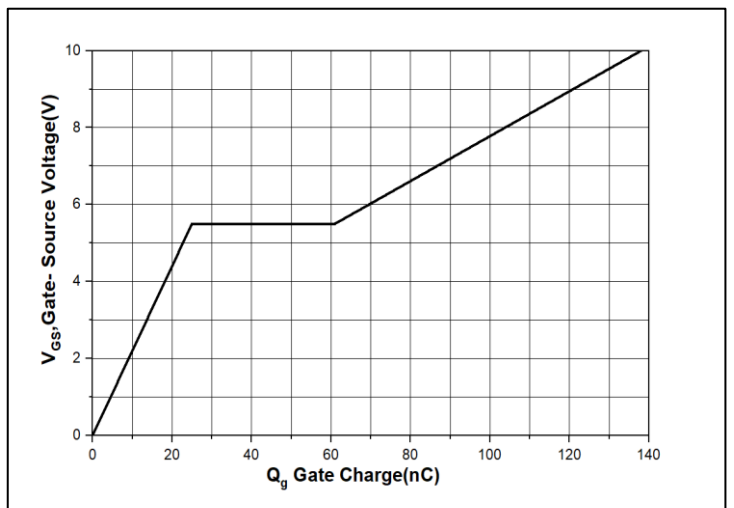


Figure6. Gate Charge

Typical Electrical and Thermal Characteristics

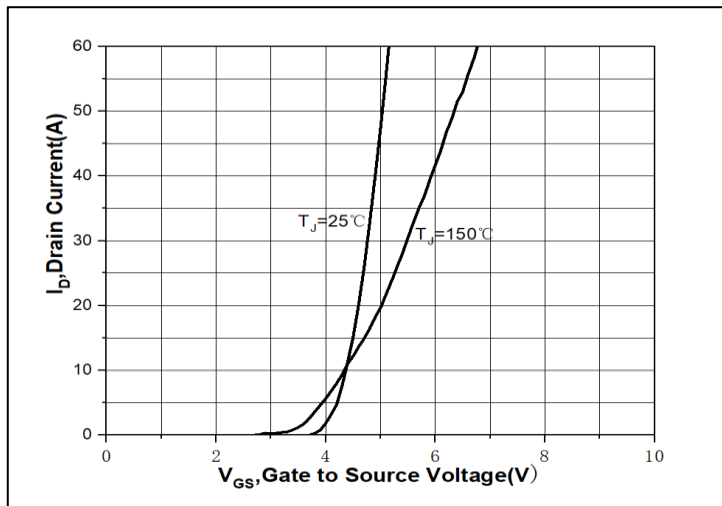


Figure7. Transfer Characteristics

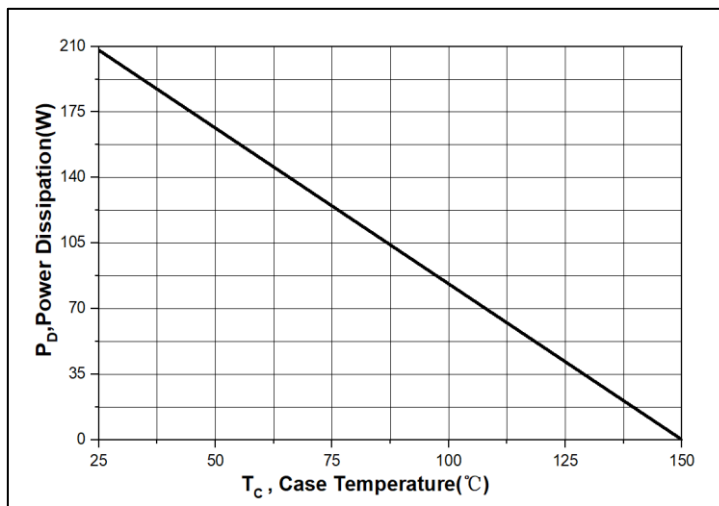
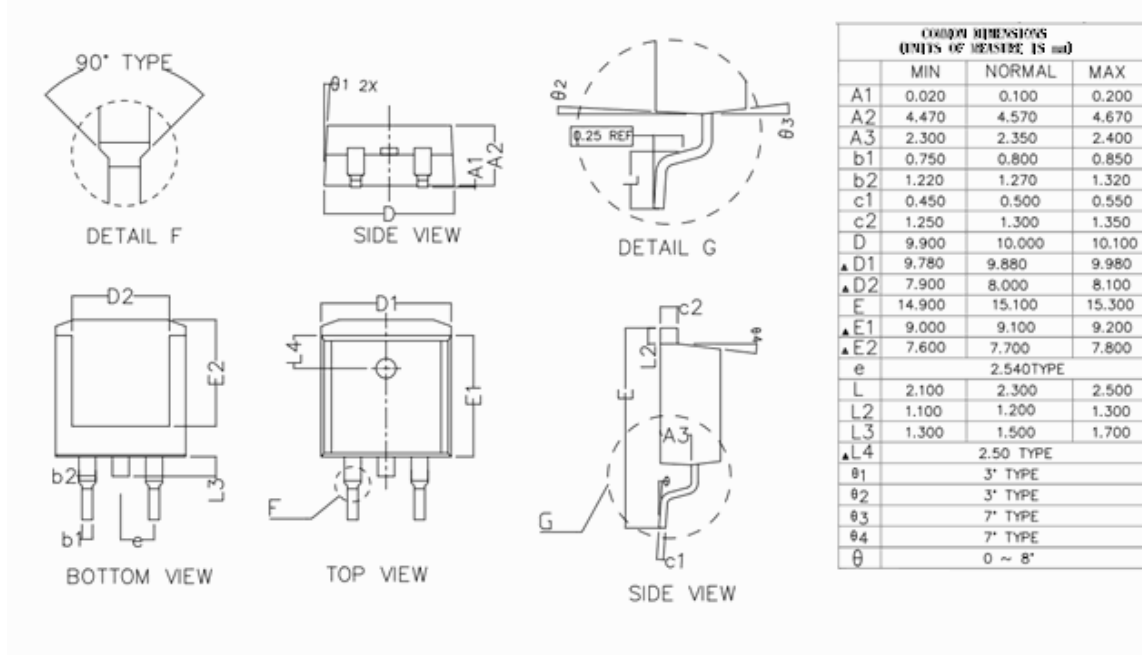


Figure8. Power Dissipation

Mechanical Data:

TO-263 Package Outline (Unit:mm)



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