

#### Main Product Characteristics:

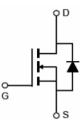
V <sub>DSS</sub>	150V
R <sub>DS</sub> (on)	4.8mΩ (typ.)
Ι <sub>D</sub>	240A



TO-220 SMS015N07A1



TO-263



SMS015N07D1

#### Schematic Diagram

#### Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



#### **Description:**

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

### **Absolute Max Rating:**

Symbol	Parameter	Max.	Units
ID @ Tc = 25°C	Continuous Drain Current, Vos @ 10V①	240	
ID @ Tc = 100°C	Continuous Drain Current, Vos @ 10V①	185	А
Ідм	Pulsed Drain Current2	720	
Pp @Tc = 25°C	Power Dissipation3	272	W
Vds	Drain-Source Voltage	150	V
Vgs	Gate-to-Source Voltage	± 20	V
Eas	Single Pulse Avalanche Energy @ L=0.5mH	1024	mJ
las	Avalanche Current	64	А
Тј Тѕтс	Operating Junction and Storage Temperature Range	-55 to +150	°C



### **Thermal Resistance**

Symbol	Characteristics	Тур.	Max.	Units
Rejc	Junction-to-case ③	_	0.46	°C/W
Reja	Junction-to-ambient ④		62	C/VV

### Electrical Characteristics @TA=25°Cunless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
V(BR)DSS	Drain-to-Source breakdown voltage	150	_	_	V	Vgs = 0V, Id = 250µA
RDS(on)	Static Drain-to-Source on-resistance	_	4.8	5.8	mΩ	Vgs=10V, Id=40A
VGS(th)	Gate threshold voltage	2	_	4	V	VDS=VGS, ID=250uA
ldss	Drain-to-Source leakage current Tj=25°C	_	_	1	μA	VDS=140V, VGS=0V
1		_	_	100	- 1	Vgs=20V, Vds=0V
lgss	Gate-to-Source forward leakage	_	_	-100	nA	Vgs=-20V, Vds=0V
Qg	Total gate charge	_	66	_		Vgs=10V, Vds=75V, Id=70A
Qgs	Gate-to-Source charge	_	21	_	nC	
Qgd	Gate-to-Drain("Miller") charge	_	20	_		
<b>t</b> d(on)	Turn-on delay time	_	18	_		V <sub>GS</sub> =10V V <sub>DS</sub> =75V R <sub>G</sub> =3Ω R <sub>L</sub> =1.07Ω
tr	Rise time	_	21	_		
td(off)	Turn-Off delay time	_	36	—	ns	
tr	Fall time	_	10	_		
Ciss	Input capacitance	_	4196	_		Vgs=0V
Coss	Output capacitance		2875	—	pF	V <sub>DS</sub> =25V f=1MHz
Crss	Reverse transfer capacitance	_	210			

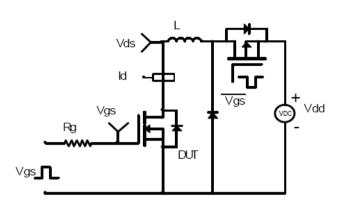
## **Source-Drain Ratings and Characteristics**

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
ls	Continuous Source Current (Body Diode)	_	_	240	А	MOSFET symbol	
Іѕм	Pulsed Source Current (Body Diode)	_	_	720	A	integral reverse E	
Vsd	Diode Forward Voltage	—	—	1.2	V	Is=20A, Vgs=0V	
trr	Reverse Recovery Time	_	101		ns	l⊧= 20A, dl/dt=500A/µs	
Qrr	Reverse Recovery Charge	_	1240		nC	i⊧= 20A, ui/ui=500A/µs	

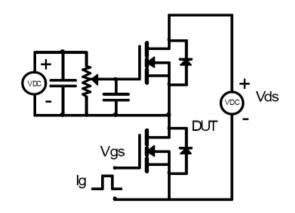


#### **Test Circuits and Waveforms**

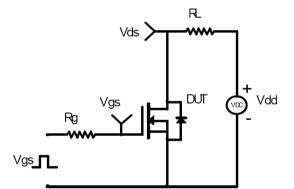
EAS Test Circuit:



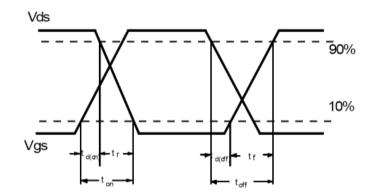
Gate Charge Test Circuit:



Switching Time Test Circuit:



Switching Waveforms:



#### Notes:

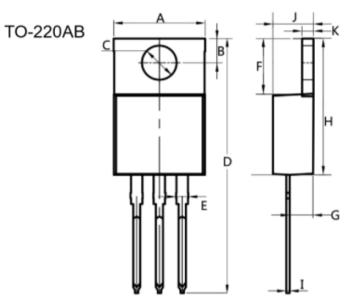
- ①Calculated continuous current based on maximum allowable junction temperature.
- 2 Repetitive rating; pulse width limited by max. junction temperature.
- ③The power dissipation P<sub>D</sub> is based on max. junction temperature, using junction-to-case thermal resistance.
- (4) The value of  $R_{\theta JA}$  is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^{\circ}$ C.



### **Mechanical Data:**

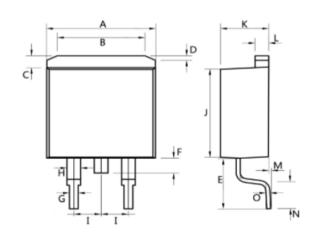
Product ID	Package
SMS015N07A1	TO-220
SMS015N07D1	TO-263

#### Unit:mm



Dim.	Min.	Max.		
A	10.0	10.4		
В	2.5	3.0		
С	3.5	4.0		
D	28.0	30.0		
E	1.1	1.5		
F	6.2	6.6		
G	2.9	3.3		
н	15.0	16.0		
I	0.35	0.45		
J	4.3	4.7		
к	1.2	1.4		
All Dimensions in millimeter				





Dim.	Min.	Max.		
A	10.0	10.5		
В	7.25	7.75		
С	1.3	1.5		
D	0.55	0.75		
E	5.0	6.0		
F	1.4	1.6		
G	0.75	0.95		
н	1.15	1.35		
I	Тур	2.54		
J	8.4	8.6		
К	4.4	4.6		
L	1.25	1.45		
M	0.02	0.1		
N	2.4	2.8		
0	0.35	0.45		
All Dimensions in millimeter				



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