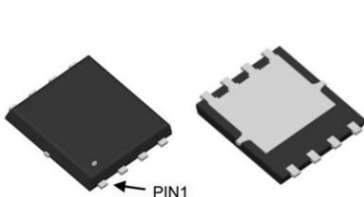
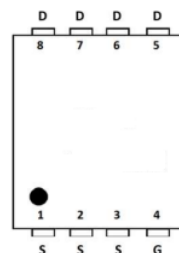


## Main Product Characteristics

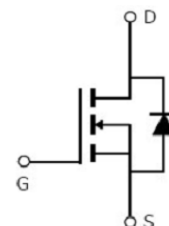
$V_{DSS}$	30V
$R_{DS(on)}$	2.4m $\Omega$ (typ.)
$I_D$	90A ①



PDFN 5x6-8L



Pin Assignment



Schematic Diagram

## Features and Benefits

- Advanced Process Technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature



## Description

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

## Absolute Max Rating

Symbol	Parameter	Max.	Units
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ①	90	A
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$ ①	59	
$I_{DM}$	Pulsed Drain Current ②	360	
$P_D @ T_C = 25^\circ\text{C}$	Power Dissipation ③	39	W
$V_{DS}$	Drain-Source Voltage	30	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$E_{AS}$	Single Pulse Avalanche Energy @ $L=0.5\text{mH}$	225	mJ
$T_J \quad T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

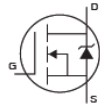
## Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-case ③	—	3.2	$^{\circ}\text{C/W}$

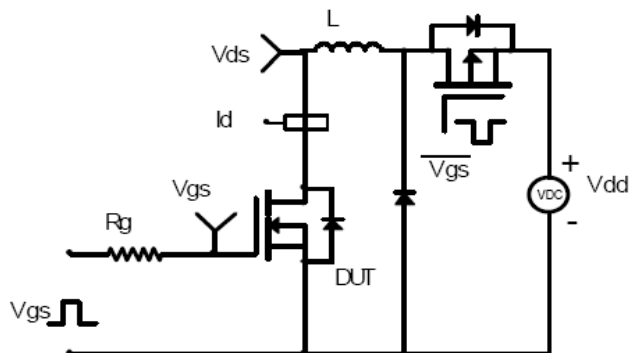
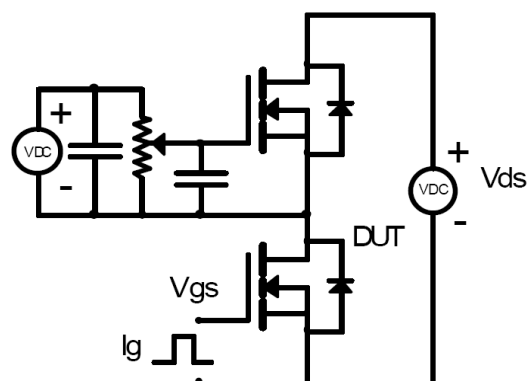
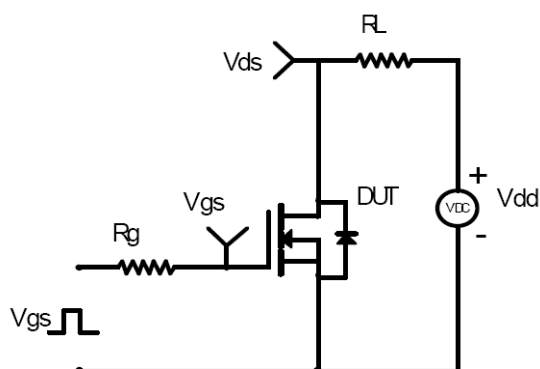
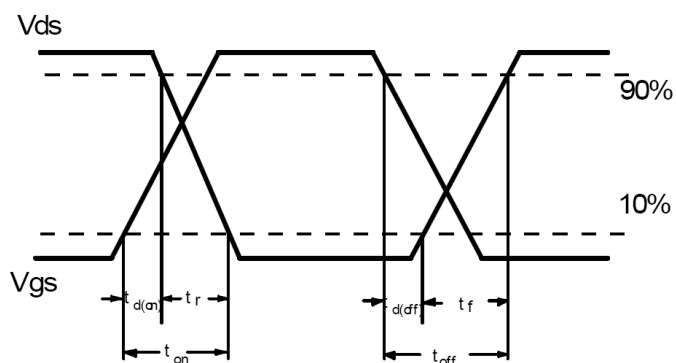
## Electrical Characterizes @ $T_A=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	30	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$R_{DS(on)}$	Static Drain-to-Source on-resistance	—	2.4	3.2	m $\Omega$	$V_{GS}=10V, I_D=30A$
		—	4.5	6.2		$V_{GS}=4.5V, I_D=20A$
$V_{GS(th)}$	Gate threshold voltage	1	1.6	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
$I_{DSS}$	Drain-to-Source leakage current	—	—	1	$\mu A$	$V_{DS} = 30V, V_{GS} = 0V$
$I_{GSS}$	Gate-to-Source forward leakage	—	—	100	nA	$V_{GS} = 20V$
		—	—	-100		$V_{GS} = -20V$
$Q_g$	Total gate charge (4.5V)	—	35	—	nC	$I_D = 30A,$ $V_{DS}=15V,$ $V_{GS} = 10V$
$Q_{gs}$	Gate-to-Source charge	—	8	—		
$Q_{gd}$	Gate-to-Drain("Miller") charge	—	12	—		
$t_{d(on)}$	Turn-on delay time	—	25	—	nS	$V_{GS}=10V, V_{DD} = 15V,$ $R_{GEN}=3\Omega$ $I_D = 30A$
$t_r$	Rise time	—	23	—		
$t_{d(off)}$	Turn-Off delay time	—	90	—		
$t_f$	Fall time	—	38	—		
$C_{iss}$	Input capacitance	—	3500	—	pF	$V_{GS} = 0V$ $V_{DS} = 15V$ $f = 1MHz$
$C_{oss}$	Output capacitance	—	500	—		
$C_{riss}$	Reverse transfer capacitance	—	428	—		

## Source-Drain Ratings and Characteristics

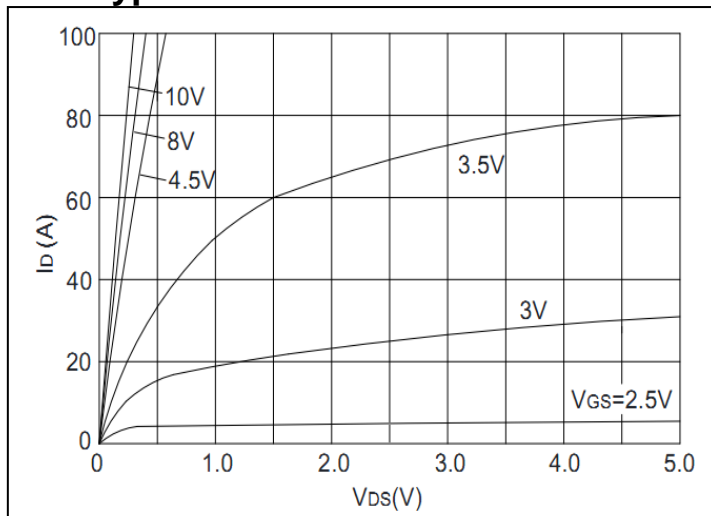
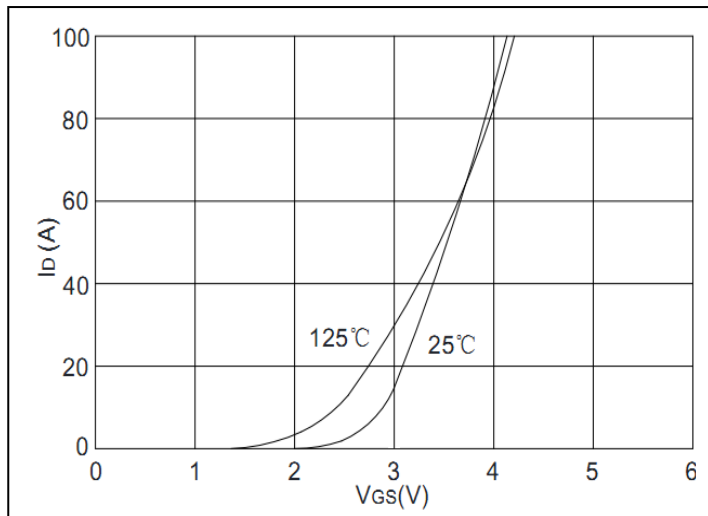
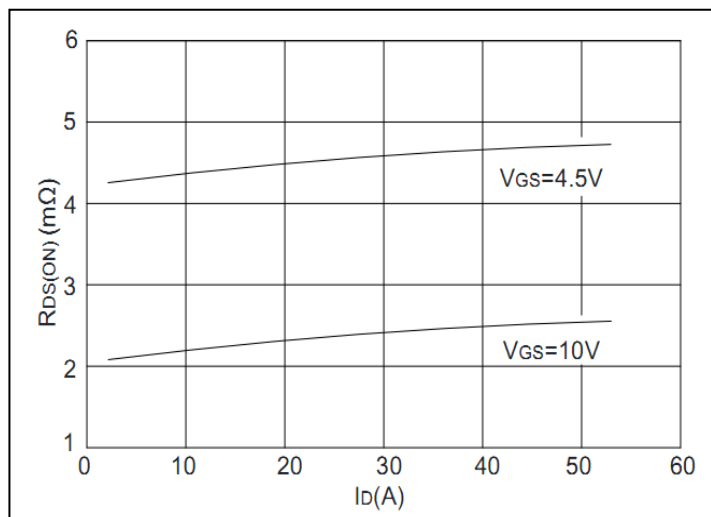
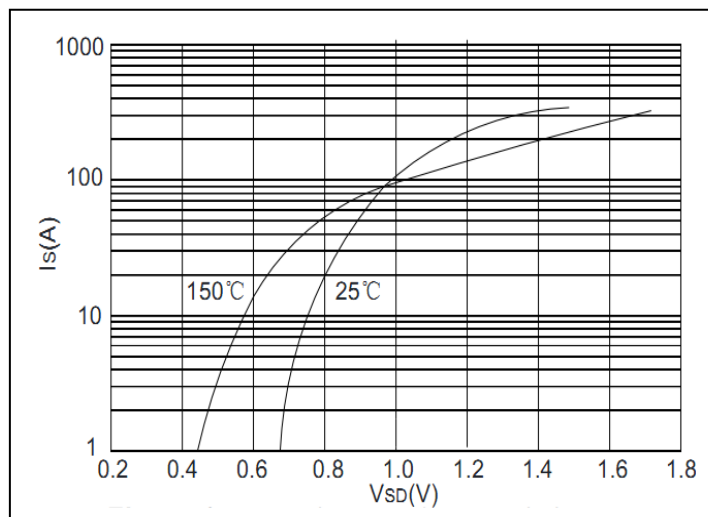
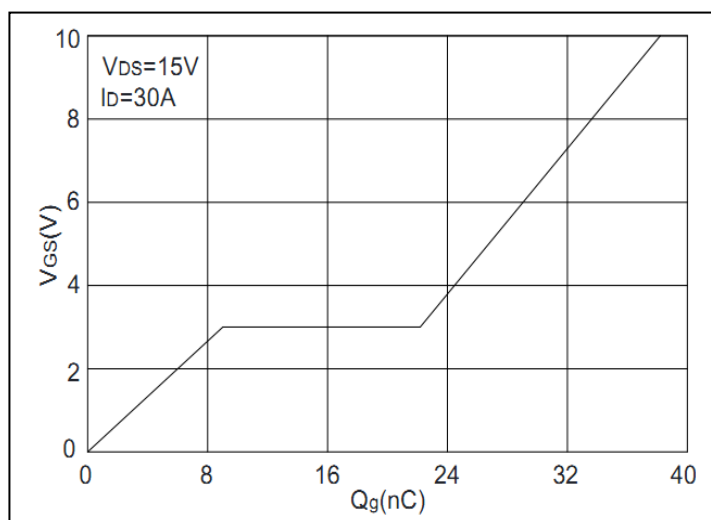
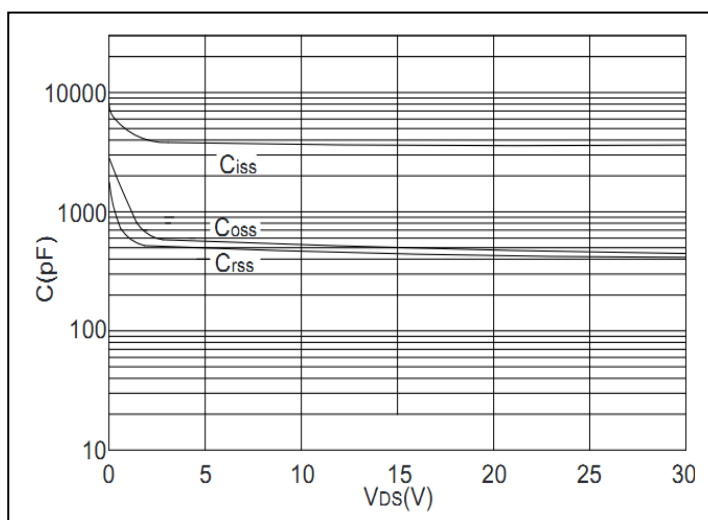
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$I_S$	Continuous Source Current (Body Diode) ①	—	—	90	A	MOSFET symbol showing the integral reverse p-n junction diode. 
$I_{SM}$	Pulsed Source Current (Body Diode) ①	—	—	360	A	
$V_{SD}$	Diode Forward Voltage	—	—	1.2	V	$I_S=30A, V_{GS}=0V, T_J = 25^{\circ}\text{C}$
$t_{rr}$	Reverse Recovery Time	—	40	—	ns	$I_S=20A, di/dt=100A/us$
$Q_{rr}$	Reverse Recovery Charge	—	38	—	nC	

## Test circuits and Waveforms

**EAS Test Circuit:**

**Gate charge test circuit:**

**Switching Time Test Circuit:**

**Switching Waveforms:**


### Notes:

- ① Calculated continuous current based on maximum allowable junction temperature.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.

**Typical Electrical and Thermal Characteristics**

**Figure 1. Typical Output Characteristics**

**Figure 2. Typical Transfer Characteristics**

**Figure 3. On-Resistance vs. Drain Current**

**Figure 4. Body Diode Characteristics**

**Figure 5. Gate Charge Characteristics**

**Figure 6. Capacitance Characteristics**

Typical Electrical and Thermal Characteristics

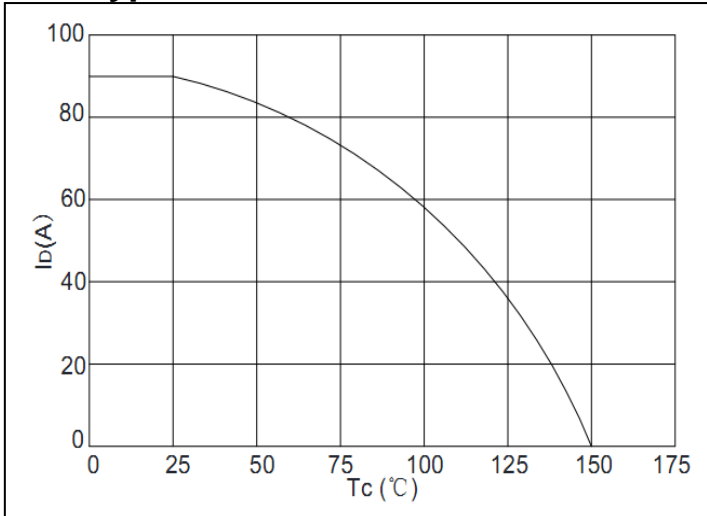


Figure 7. Maximum Continuous Drain Current vs. Case Temperature

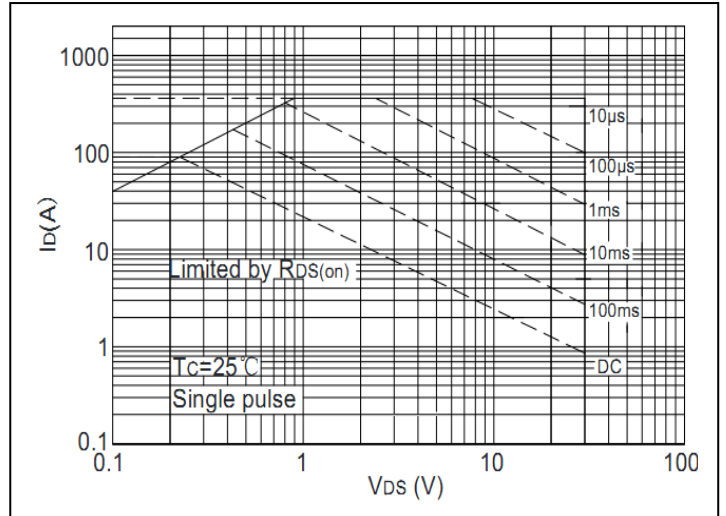


Figure 8. Safe Operating Area

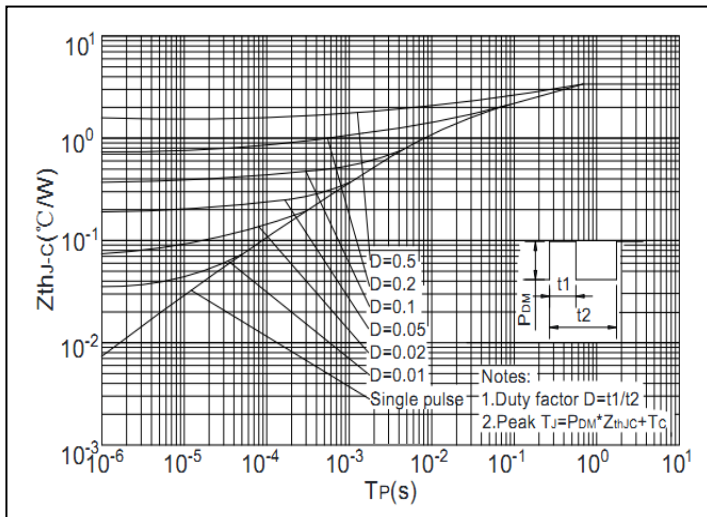
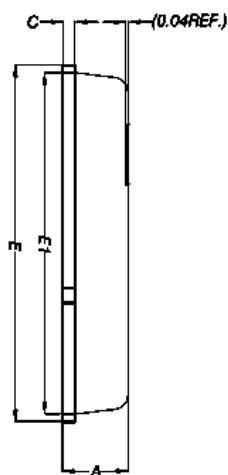
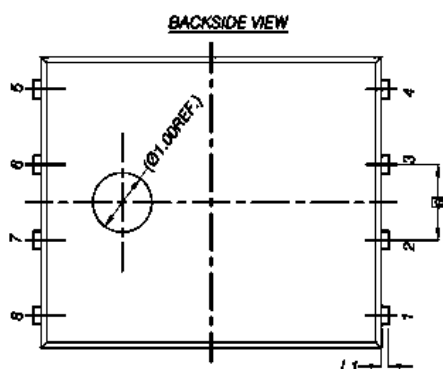
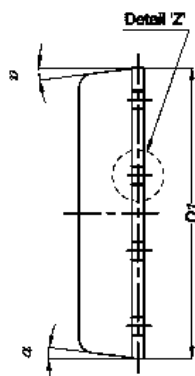
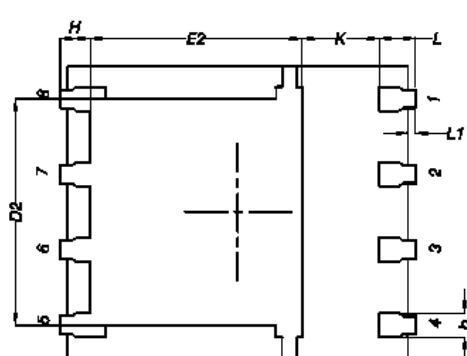


Figure 9. Normalized Maximum Transient Thermal Impedance

**Mechanical Data:**


DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
<b>e</b>	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
$\alpha$	0°	-	12°

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