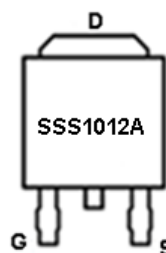
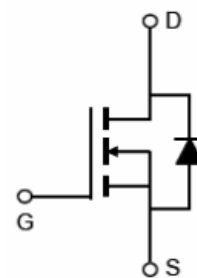


Main Product Characteristics:

V_{DSS}	100V
$R_{DS(on)}$	9m Ω (typ.)
I_D	55A



TO-263


 Marking and pin
Assignment


Schematic diagram

Features and Benefits:

- Advanced MOSFET process technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature


Description:

It utilizes the latest processing techniques to achieve the high cell density and reduces the on-resistance with high repetitive avalanche rating. These features combine to make this design an extremely efficient and reliable device for use in power switching application and a wide variety of other applications.

Absolute max Rating:

Symbol	Parameter	Max.	Units
I_D @ TC = 25°C	Continuous Drain Current, V_{GS} @ 10V ①	55	A
I_{DM}	Pulsed Drain Current ②	220	
P_D @TC = 25°C	Power Dissipation ③	56.5	W
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-to-Source Voltage	± 20	V
E_{AS}	Single Pulse Avalanche Energy @ L=0.1mH	20	mJ
T_J T_{STG}	Operating Junction and Storage Temperature Range	-55 to +150	°C

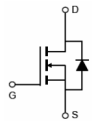
Thermal Resistance

Symbol	Characterizes	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-case ③	—	1.7	$^{\circ}\text{C}/\text{W}$
$R_{\theta JA}$	Junction-to-ambient ($t \leq 10\text{s}$) ④	—	50	$^{\circ}\text{C}/\text{W}$

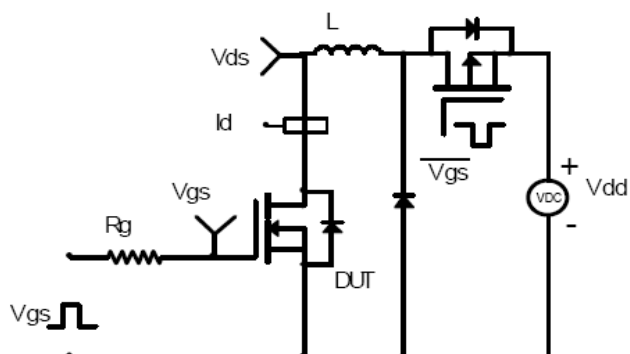
Electrical Characterizes @ $T_A=25^{\circ}\text{C}$ unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	100	—	—	V	$V_{GS} = 0\text{V}, I_D = 250\mu\text{A}$
$R_{DS(on)}$	Static Drain-to-Source on-resistance	—	9	12	m Ω	$V_{GS}=10\text{V}, I_D = 20\text{A}$
$V_{GS(th)}$	Gate threshold voltage	2.5	—	4	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
I_{DSS}	Drain-to-Source leakage current	—	—	1	μA	$V_{DS} = 100\text{V}, V_{GS} = 0\text{V}$
I_{GSS}	Gate-to-Source forward leakage	—	—	100	nA	$V_{GS} = 20\text{V}$
		—	—	-100		$V_{GS} = -20\text{V}$
Q_g	Total gate charge	—	45	—	nC	$I_D = 20\text{A},$ $V_{DS}=50\text{V},$ $V_{GS} = 10\text{V}$
Q_{gs}	Gate-to-Source charge	—	14	—		
Q_{gd}	Gate-to-Drain("Miller") charge	—	12	—		
$t_{d(on)}$	Turn-on delay time	—	10	—	ns	$V_{GS}=10\text{V}, V_{DS}=50\text{V},$ $R_{GEN}=3\Omega$ $I_D = 20\text{A}$
t_r	Rise time	—	5	—		
$t_{d(off)}$	Turn-Off delay time	—	25	—		
t_f	Fall time	—	4	—		
C_{iss}	Input capacitance	—	2400	—	pF	$V_{GS} = 0\text{V}$ $V_{DS} = 50\text{V}$ $f = 1\text{MHz}$
C_{oss}	Output capacitance	—	150	—		
C_{riss}	Reverse transfer capacitance	—	10	—		

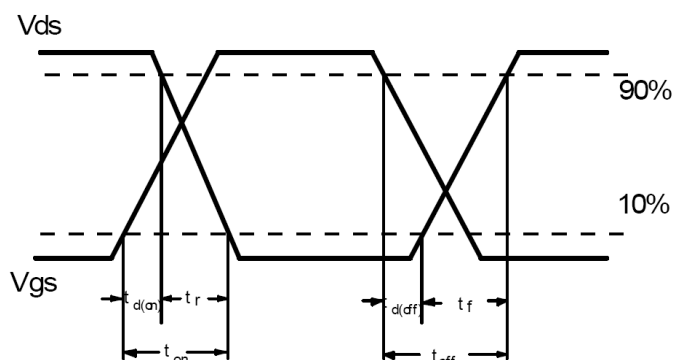
Source-Drain Ratings and Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	34	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode)	—	—	102	A	
V_{SD}	Diode Forward Voltage	—	—	1	V	$I_S=1\text{A}, V_{GS}=0\text{V}$
t_{rr}	Reverse Recovery Time	—	25	—	ns	$I_S=20\text{A}, di/dt=100\text{A}/\mu\text{s}$
Q_{rr}	Reverse Recovery Charge	—	125	—	nC	

Test circuits and Waveforms

EAS Test Circuit:

Gate charge test circuit:

Switching Time Test Circuit:

Switching Waveforms:


Notes:

- ① Calculated continuous current based on maximum allowable junction temperature.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- ④ The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$

Typical electrical and thermal characteristics

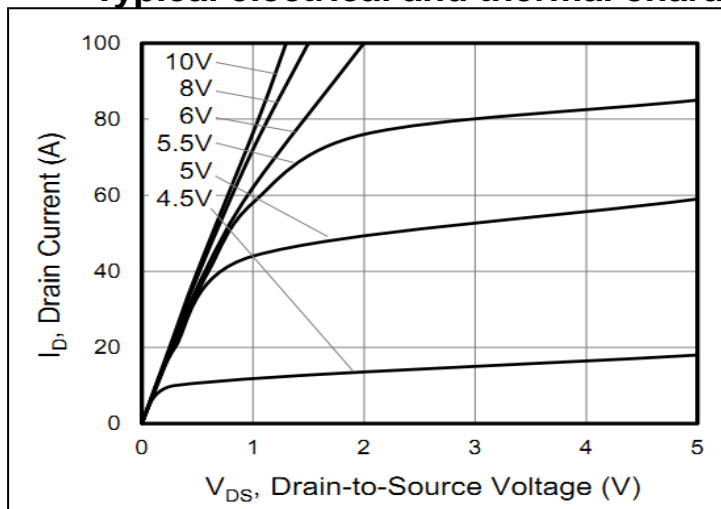


Figure 1: Typical Output Characteristics

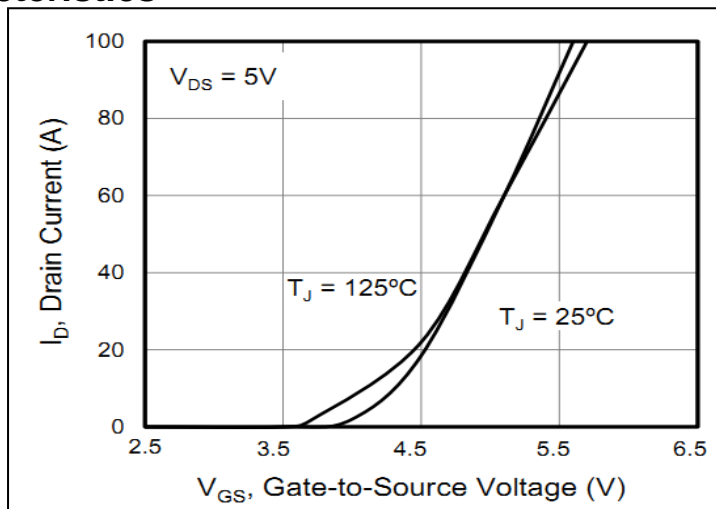


Figure 2: Transfer Characteristics

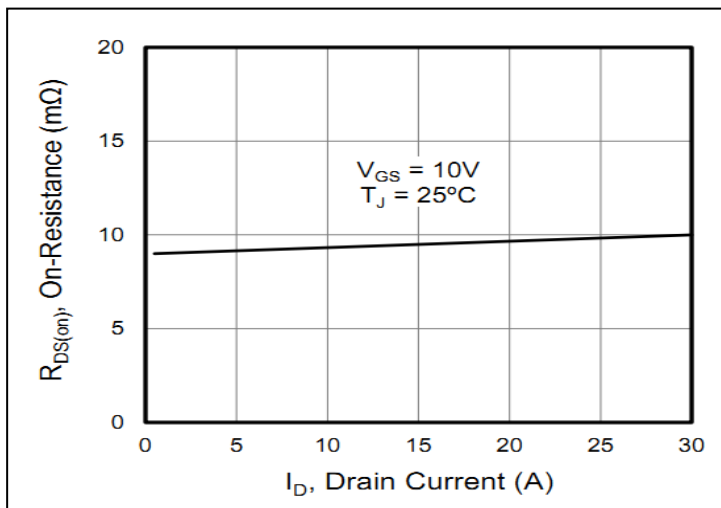


Figure 3: On-Resistance vs. Drain Current

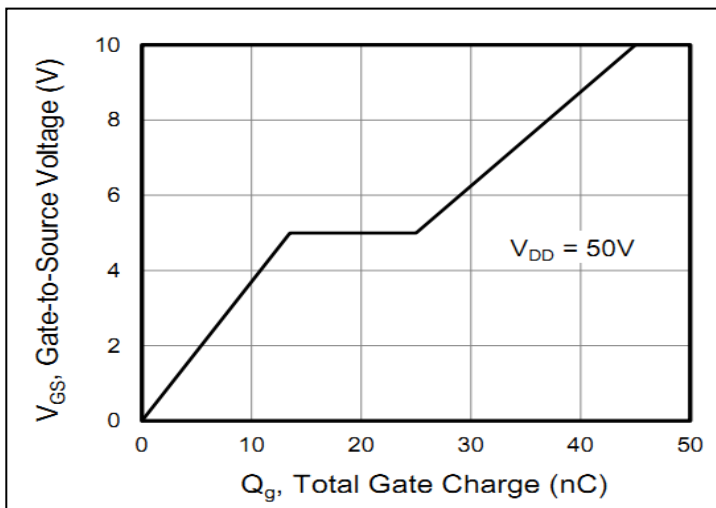


Figure 4: Gate Charge

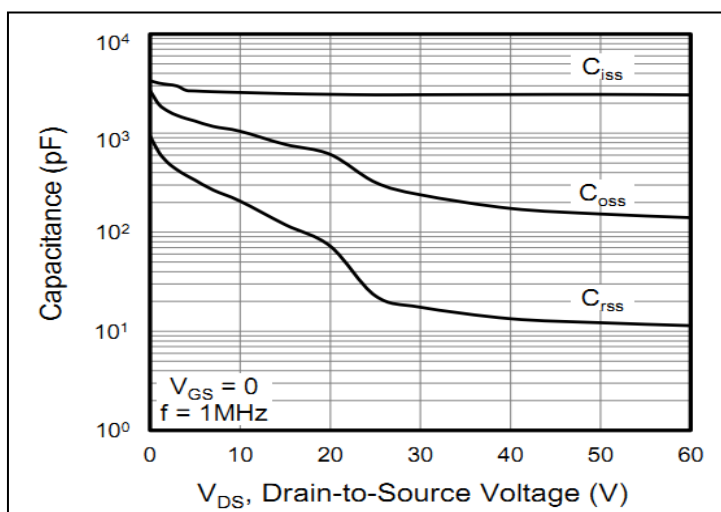


Figure 5: Capacitance

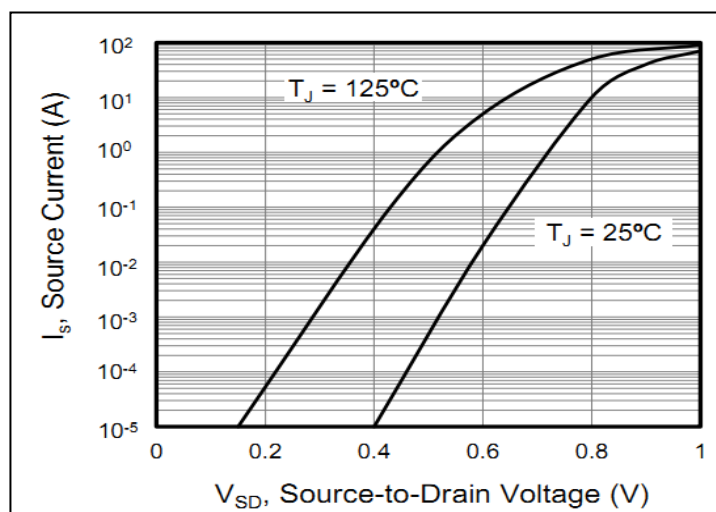


Figure 6: Body Diode Forward Voltage

Typical electrical and thermal characteristics

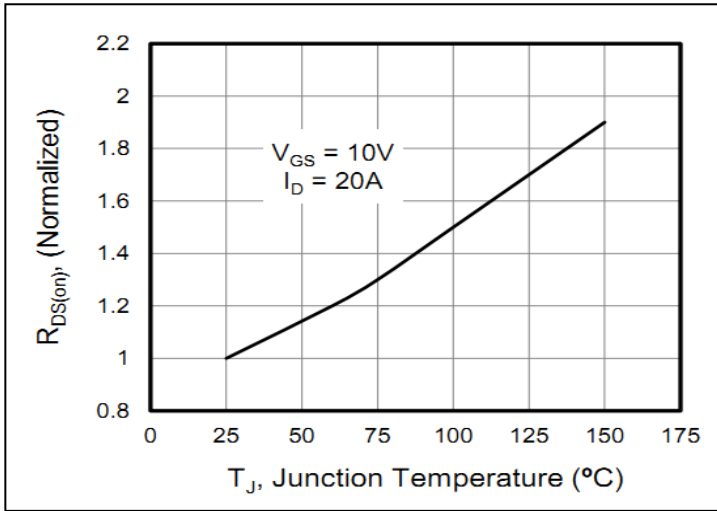


Figure 7: On-Resistance vs. Junction Temperature

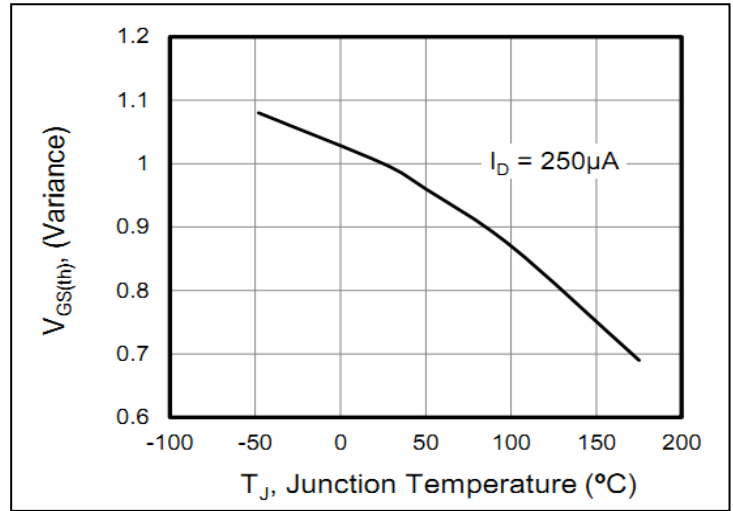


Figure 8: Threshold Voltage vs. Junction Temperature

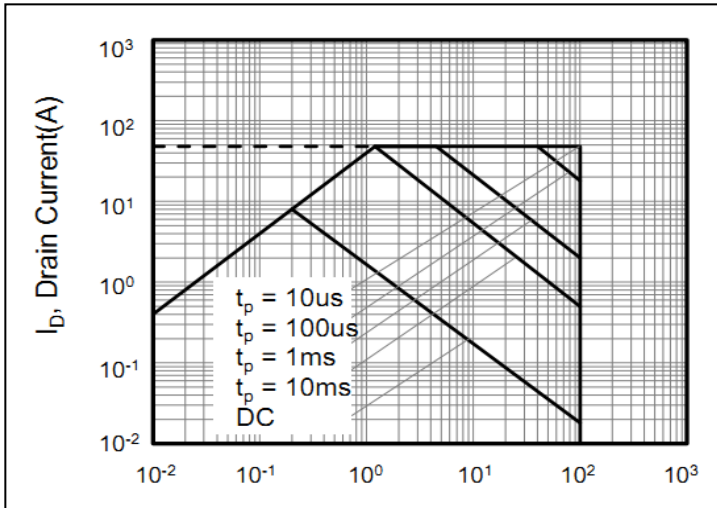


Figure 9: Safe Operation Area

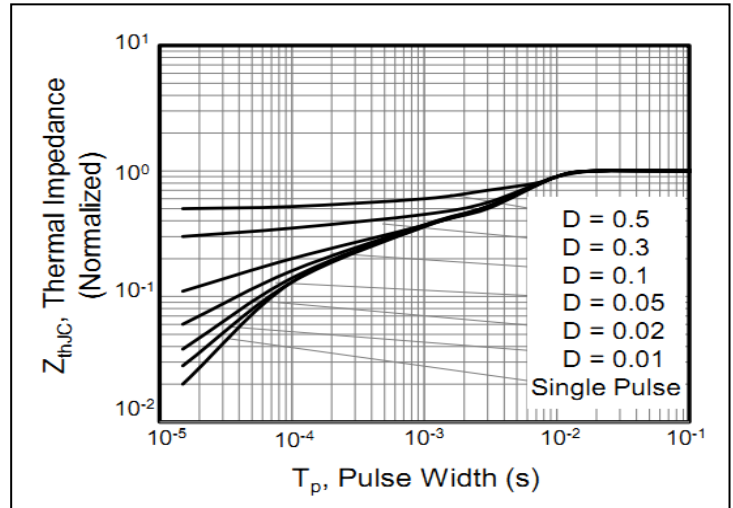
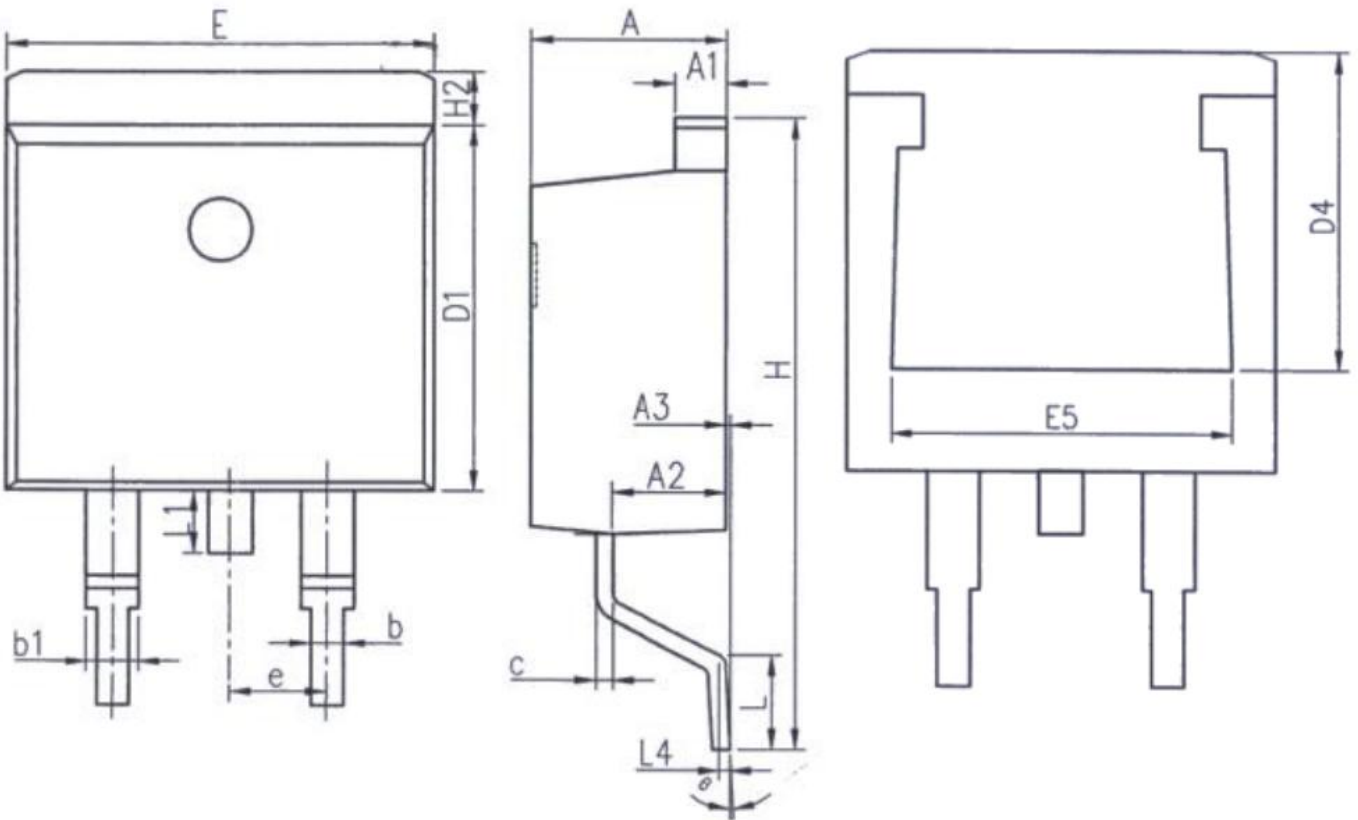


Figure 10: Transient Thermal Impedance

Mechanical Data:



Unit: mm		
Symbol	Min.	Max.
A	4.37	4.77
A1	1.22	1.42
A2	2.49	2.89
A3	0.00	0.25
b	0.70	0.96
b1	1.17	1.47
c	0.30	0.53
D1	8.50	8.90
D4	6.60	-

Unit: mm		
Symbol	Min.	Max.
E	9.86	10.36
E5	7.06	-
e	2.54BSC	
H	14.70	15.50
H2	1.07	1.47
L	2.00	2.60
L1	1.40	1.70
L4	0.25BSC	
θ	0°	9°

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